

Madanapalle Institute of Technology & Science, Madanapalle -517 325
Department of Electrical and Electronics Engineering
PUBLICATIONS : 2013-14

| S.No | Faculty Name | Co-Author | Paper/Book title | Publisher | Impact Factor | DOI | Citations |
|------|----------------------|---|--|---|---------------|--------------------------|-----------|
| 1 | A.Srinivasulu | P.Nagaraju | Load balance with loop power controller & fuel cell system in distribution feeders | IJESI | - | - | - |
| 2 | B.CHANDRA SEKHAR | A.Pushpa , B.Chandra Sekhar | Performance Analysis of Fuzzy Logic Controller Based Compensator with Power Quality Improvement Features | International Journal of Power System Operation and Energy Management | 3.43 | - | - |
| 3 | B.CHANDRA SEKHAR | B. Somashekar1, B. Chandrasekhar2, David Livingston. D3 | Modeling and Simulation of Three to Nine Phase Using Special Transformer Connection | IJETAE | 2.324 | - | - |
| 4 | B.Sreenivasa Raju | M. Lokesh | A Simplified Svpwm for Five Level Inverter with DC-Link Balancing | International Journal of Engineering and Advanced Technology (IJEAT) | 1.097 | - | - |
| 5 | C.Kamal Basha | M.Suryakalavathi | Speed sensorless vector control of Induction motor using stator current based MRAS scheme | ICEPCECE -2014 | - | - | - |
| 6 | C.Kamal Basha | M.Suryakalavathi | Estimation of Rotor Flux using Neural Network Observer in Speed Sensorless Induction Motor Drive | International Journal of Computer Applications (0975 – 8887) | - | - | - |
| 7 | D Chinnakullay reddy | U Naresh babu , M.Suryakalavathi,V.Ganesh | A BRIEF REVIEW & STUDY OF PowerQuality: Distributed Generation | AES Publications | 1.1419 | - | - |
| 8 | D Chinnakullay reddy | J.Praveen | Kalman Filter Based Unified Power Quality Conditioner for Output Regulation | RIPE Publications | - | 10.15598/ae e.v13i4.1486 | - |
| 9 | K.Vasu | V.Ganesh | MODEL MATCHING CONTROL OF TWO-AREA LFC | International Journal of Advancements in Electronics and Electrical Engineering | - | - | - |
| 10 | M. Lokesh | B.Sreenivasa Raju | A Simplified Svpwm for Five Level Inverter with DC-Link Balancing | International Journal of Engineering and Advanced Technology (IJEAT) | 1.097 | - | - |